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November 2013

FQD5N20L

N-Channel QFET® MOSFET

200 V, 3.8 A, 1.2 Ω

Description

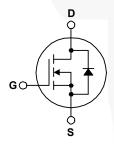
This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance • Low Crss (Typ. 6.0 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power • 100% Avalanche Tested factor correction (PFC), and electronic lamp ballasts.

Features

- 3.8 A, 200 V, $R_{DS(on)} = 1.2 \Omega$ (Max.) @ $V_{GS} = 10 V$, $I_D = 1.9 A$
- Low Gate Charge (Typ. 4.8 nC)

- · RoHS Compliant





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter	/-	FQD5N20LTM	Unit
V _{DSS}	Drain-Source Voltage		200	V
I _D	Drain Current - Continuous (T _C = 25°C)		3.8	Α
	- Continuous (T _C = 100°C)		2.4	Α
I _{DM}	Drain Current - Pulsed	(Note 1)	15.2	Α
V _{GSS}	Gate-Source Voltage		± 20	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		60	mJ
I _{AR}	Avalanche Current		3.8	Α
E _{AR}	Repetitive Avalanche Energy (N		3.7	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		5.5	V/ns
P _D	Power Dissipation (T _A = 25°C) *		2.5	W
	Power Dissipation (T _C = 25°C)		37	W
	- Derate above 25°C		0.29	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum lead temperature for soldering, 1/8" from case for 5 seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FQD5N20LTM	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.4	
В	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	110	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (*1 in ² Pad of 2-oz Copper), Max.	50	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQD5N20LTM	FQD5N20L	DPAK	Tape and Reel	330 mm	16 mm	2500 units

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		0.18		V/°C
I _{DSS}	7 0 1 1/1 5 1 0 1	V _{DS} = 200 V, V _{GS} = 0 V			1	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = 160 V, T _C = 125°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1.0		2.0	V
R _{DS(on)}	Static Drain-Source	V _{GS} = 10 V, I _D = 1.9 A		0.94	1.2	
DO(011)	On-Resistance	V _{GS} = 5 V, I _D = 1.9 A		0.98	1.25	Ω
g _{FS}	Forward Transconductance	V _{DS} = 30 V, I _D = 1.9 A		3.35		S
Dynam	ic Characteristics					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,	1	250	325	pF
C _{oss}	Output Capacitance	f = 1.0 MHz		40	50	pF
C _{rss}	Reverse Transfer Capacitance	- · · · · · · · · · · · · · · · · · · ·		6	8	pF
Switchi	ing Characteristics					
t _{d(on)}	Turn-On Delay Time	V 400 V 1 4 5 A		9	25	ns
t _r	Turn-On Rise Time	$V_{DD} = 100 \text{ V}, I_D = 4.5 \text{ A},$		90	190	ns
t _{d(off)}	Turn-Off Delay Time	$R_G = 25 \Omega$		15	40	ns
t _f	Turn-Off Fall Time	(Note 4)		50	110	ns
Qg	Total Gate Charge	V _{DS} = 160 V, I _D = 4.5 A,		4.8	6.2	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 5 V		1.2		nC
Q _{gd}	Gate-Drain Charge	(Note 4)		2.4		nC
	Source Diode Characteristics a	nd Maximum Ratings				
I _S	Maximum Continuous Drain-Source Diode Forward Current				3.8	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				15.2	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 3.8 A			1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 4.5 A,		95		ns
Q _{rr}	Reverse Recovery Charge	dI _F / dt = 100 A/μs		0.3		μС

- $$\label{eq:Notes:1} \begin{split} &\textbf{Notes:}\\ &\textbf{1.} \ \text{Repetitive rating: pulse-width limited by maximum junction temperature.}\\ &\textbf{2.} \ \textbf{L} = \textbf{6.2 mH, I}_{AS} = \textbf{3.8 A, V}_{DD} = \textbf{50 V, R}_{G} = \textbf{25 } \Omega, \ \text{starting T}_{J} = \textbf{25}^{\circ}\text{C.}\\ &\textbf{3.} \ \textbf{I}_{SD} \leq \textbf{4.5 A, di/dt} \leq \textbf{300 A/\mus, V}_{DD} \leq \textbf{BV}_{DSS, starting T}_{J} = \textbf{25}^{\circ}\text{C.}\\ &\textbf{4.} \ \text{Essentially independent of operating temperature.} \end{split}$$

Typical Characteristics

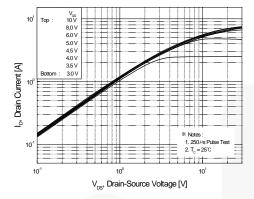


Figure 1. On-Region Characteristics

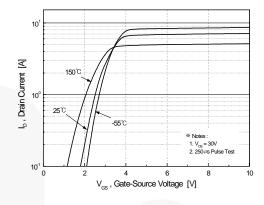


Figure 2. Transfer Characteristics

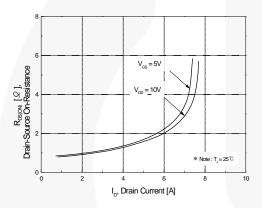


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

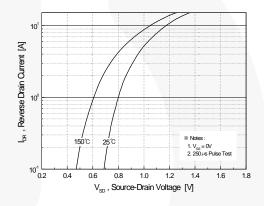


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

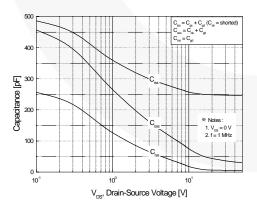


Figure 5. Capacitance Characteristics

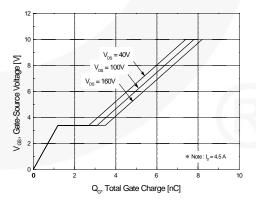


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

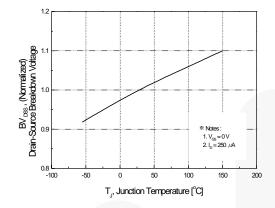


Figure 7. Breakdown Voltage Variation vs. Temperature

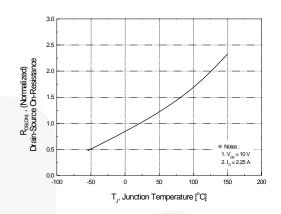


Figure 8. On-Resistance Variation vs. Temperature

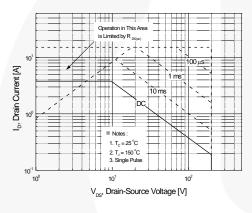


Figure 9. Maximum Safe Operating Area

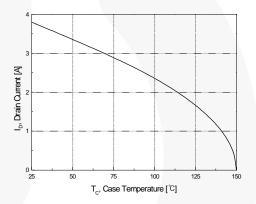


Figure 10. Maximum Drain Current vs. Case Temperature

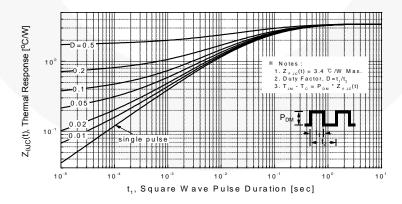


Figure 11. Transient Thermal Response Curve

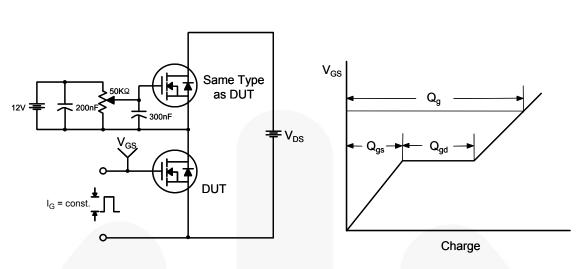


Figure 12. Gate Charge Test Circuit & Waveform

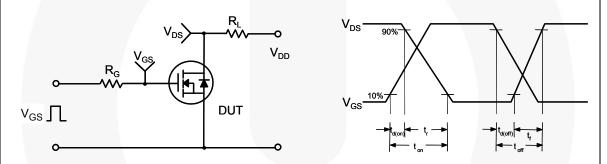


Figure 13. Resistive Switching Test Circuit & Waveforms

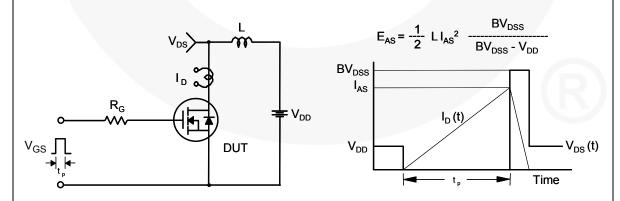
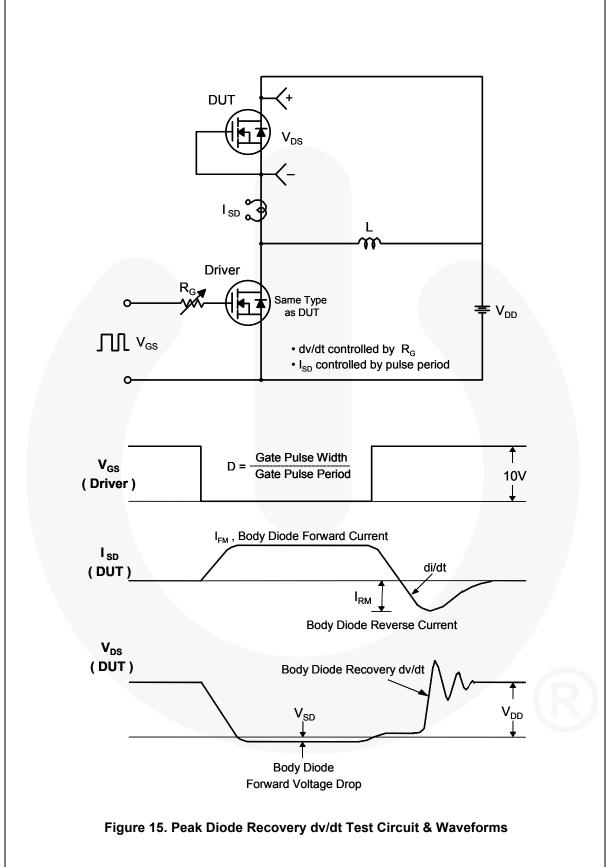


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

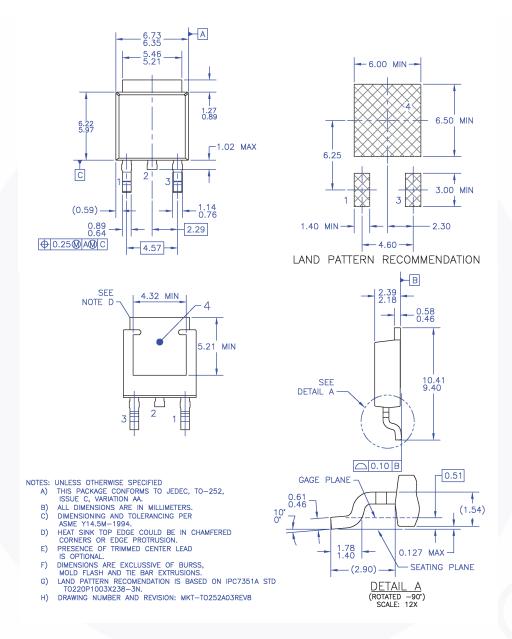


Figure 16. TO252 (D-PAK), Molded, 3-Lead, Option AA&AB

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