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November 2013

FQU2N50B

N-Channel QFET® MOSFET

500 V, 1.6 A, 5.3 Ω

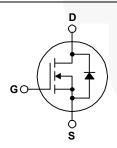
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 1.6 A, 500 V, $R_{DS(on)}$ = 5.3 Ω (Max.) @ V_{GS} = 10 V, I_D = 0.8 A
- Low Gate Charge (Typ. 6.0 nC)
- · Low Crss (Typ. 4.3 pF)
- Fast Switching
- · 100% Avalanche Tested
- · Improved dv/dt Capability





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQU2N50BTU_WS	Units
V_{DSS}	Drain-Source Voltage		500	V
I _D	Drain Current - Continuous (T _C = 25°C))	1.6	Α
	- Continuous (T _C = 100°C	C)	1.0	Α
I _{DM}	Drain Current - Pulsed	(Note 1)	6.4	Α
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy (Note		120	mJ
I _{AR}	Avalanche Current	(Note 1)	1.6	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	3.0	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
P _D	Power Dissipation (T _A = 25°C) *		2.5	W
	Power Dissipation (T _C = 25°C)		30	W
	- Derate above 25°C		0.24	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FQU2N50BTU_WS	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	4.17	
В	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	110	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (*1 in ² Pad of 2-oz Copper), Max.	50	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQU2N50BTU_WS	FQU2N50B	I-PAK	Tube	N/A	N/A	75 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	500			V
ΔBV_{DSS} / ΔT_{J}	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$, Referenced to 25°C		0.48		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 500 V, V _{GS} = 0 V			1	μΑ
		V _{DS} = 400 V, T _C = 125°C		-	10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	2.3	3.0	3.7	V
(· /		$V_{DS} = V_{GS}, I_{D} = 250 \text{ mA}$	3.6	4.3	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 0.8 A		4.2	5.3	Ω
9 _{FS}	Forward Transconductance	$V_{DS} = 50 \text{ V}, I_{D} = 0.8 \text{ A}$		1.3		S
<u> </u>	ic Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		180	230	pF
C _{oss}	Output Capacitance	f = 1.0 MHz		30	40	pF
C _{rss}	Reverse Transfer Capacitance			4	6	pF
Switchi	ing Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 250 V, I _D = 2.1 A,		6	20	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		25	60	ns
t _{d(off)}	Turn-Off Delay Time			10	30	ns
t _f	Turn-Off Fall Time	(Note 4)		20	50	ns
Q_g	Total Gate Charge	$V_{DS} = 400 \text{ V}, I_{D} = 2.1 \text{ A},$		6.0	8.0	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V	/	1.3		nC
Q_{gd}	Gate-Drain Charge	(Note 4)	/	3.0		nC
Drain-S	Source Diode Characteristics a	nd Maximum Ratings				
I _S	Maximum Continuous Drain-Source Diode Forward Current				1.6	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				6.4	Α
	Danis Ossans Disala Farmand Vallana	$V_{GS} = 0 \text{ V}, I_{S} = 1.6 \text{ A}$			1.4	V
V_{SD}	Drain-Source Diode Forward Voltage	103 - 1,13				
V _{SD}	Reverse Recovery Time	$V_{GS} = 0 \text{ V, } I_S = 2.1 \text{ A,}$		195		ns

- Notes: 1. Repetitive rating : pulse-width limited by maximum junction temperature. 2. L = 85 mH, I_{AS} = 1.6 A, V_{DD} = 50 V, R_{C} = 25 Ω , starting T_{J} = 25°C. 3. I_{SD} ≤ 2.1 A, di/dt ≤ 200 A/ μ s, V_{DD} ≤ BV $_{DSS}$, starting T_{J} = 25°C. 4. Essentially independent of operating temperature.

Typical Characteristics

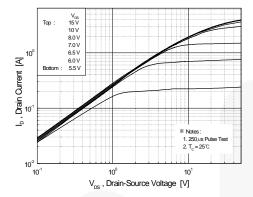


Figure 1. On-Region Characteristics

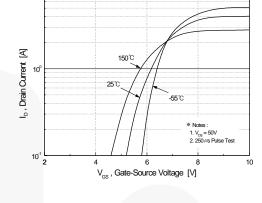


Figure 2. Transfer Characteristics

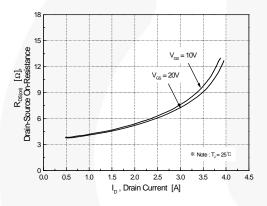


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

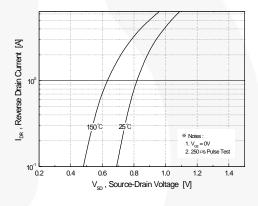


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

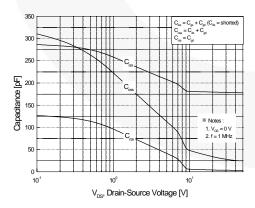


Figure 5. Capacitance Characteristics

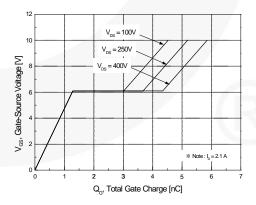


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

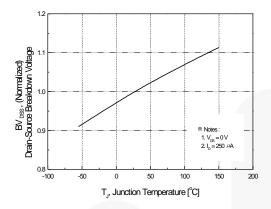


Figure 7. Breakdown Voltage Variation vs. Temperature

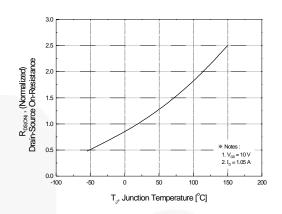


Figure 8. On-Resistance Variation vs. Temperature

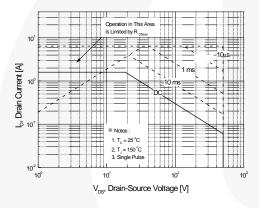


Figure 9. Maximum Safe Operating Area

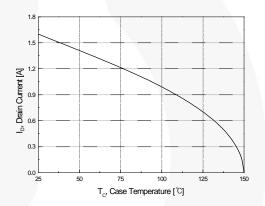


Figure 10. Maximum Drain Current vs. Case Temperature

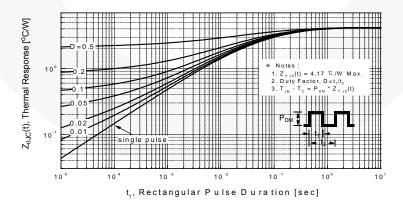


Figure 11. Transient Thermal Response Curve

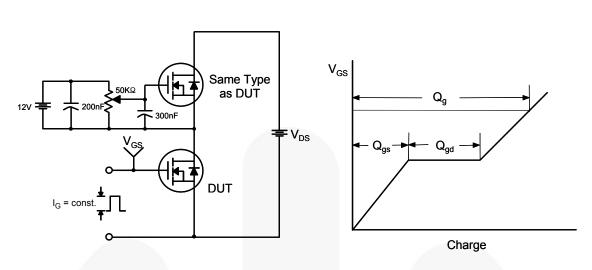


Figure 12. Gate Charge Test Circuit & Waveform

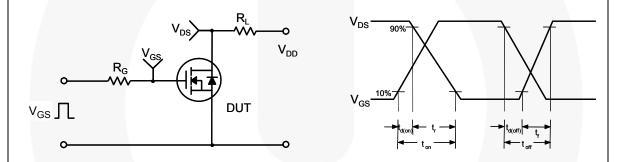


Figure 13. Resistive Switching Test Circuit & Waveforms

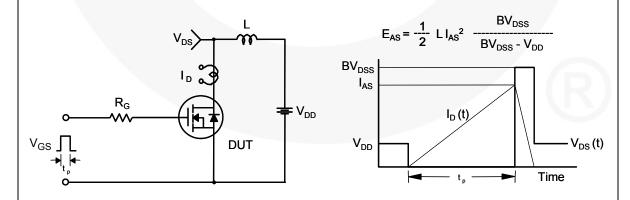
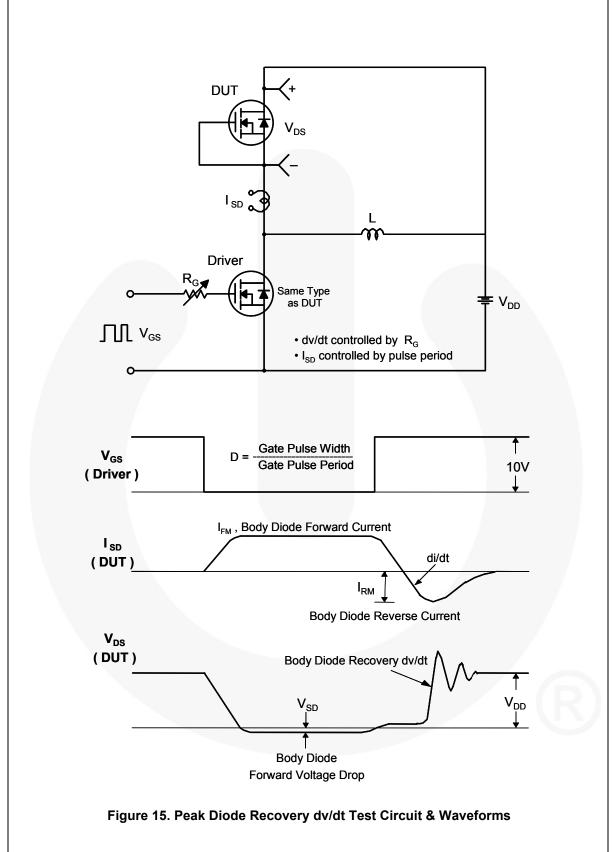


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

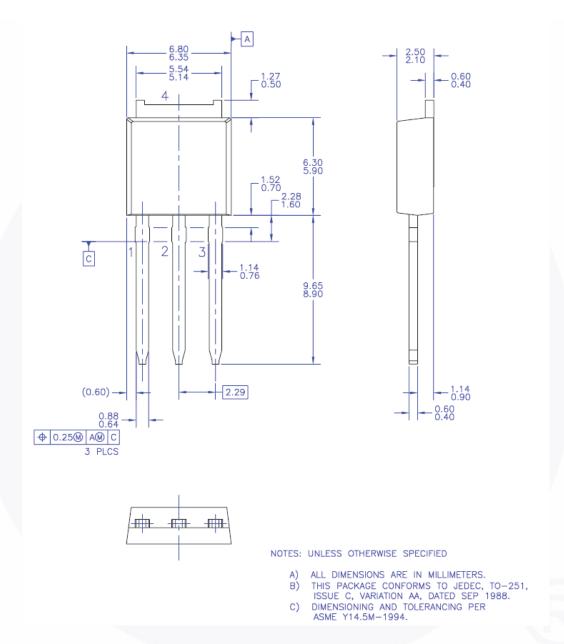


Figure 16. TO-251 (I-PAK), Molded, 3-Lead, Option AA

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